

ABSTRACT

A key generation section 10 generates a key K to be used for cryptographic processing, based on an encrypted key data group
5 EK. A key information retention/selection section 20 retains the generated key and an intermediate key which was obtained when generating the key, and outputs the retained key information in accordance with selection information SEL. The key information is retained, e.g., in a storage circuit within the integrated
10 circuitry, in a form which is not recognizable as a key. A content encryption/decryption section 30 restrains the cryptographic processing result DO from being outputted during key generation. When a heading pattern is detected during the cryptographic processing of an input signal DI, a reset/input enable signal
15 generation circuit 60 switches an input enable signal IE to a disabled state, and outputs a reset signal RST after the cryptographic processing has been completed.